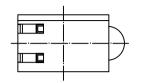
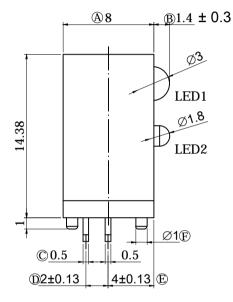
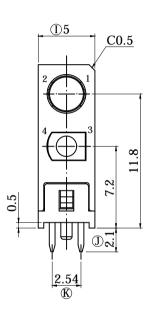
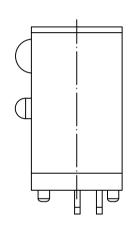
DIMENSION	TOLERANCE
BELOW 10 mm	± 0.25
10~100 mm	± 0.25
CRITICAL DIMEN	sions : 'A' ~ 'K'

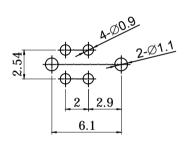


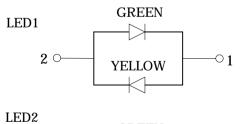






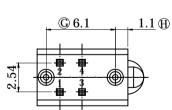








A					DATA	2019/12/26	UNIT	mm	MODE		
2					APPROVAL	KAVEN	SCALE	1:1	PART	LED003-KYGUG-TR	<b>WR</b> Well Buying
$\triangle$					CONFIRM	EASON	VIEW	$\oplus$	2D FILE NAME	LED003-KYGUG-TR	<b>VV</b> $D$ <sub>Industrial Co.,Ltd.</sub>
	DATA	APPROVAL	DESIGN	ENGINEERING CHANGE DESCRIPTION	DESIGN	YJS	VER.	01	3D FILE NAME		



## WELL BUYING INDUSTRIAL CO., LTD. SPECIFICATIONS OF LED003 SERIES

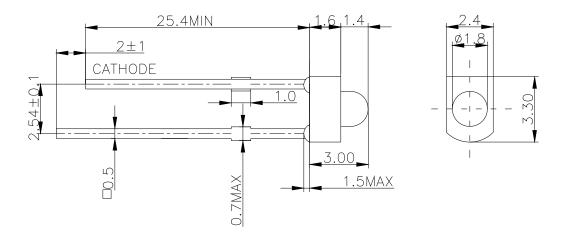
### 1. PACKING AND SHIPPING:

LED003	200 PCS / 1 REEL		
	8 REEL / 1 CTN		
	1600 PCS/ 1 CTN		
CARTON SIZE	35*35*32.5CM CM		

#### ♦Features

- •2mm Nipple LED Lamps
- •Emitting Color: Green
- •Lens Color: Green Diffuse
- Mertial:InGaN
- •Low power consumption
- •Excellent product quality and reliability
- •Lead-free device
- ♦Applications
- Electronic signs and signals
- Bright ambient lighting conditions
- Backlight
- General purpose indicatiors

### • Package Dimensions



Notes:

- 1. All dimensions are in millimeters.
- 2. Tolerance is  $\pm 0.25$  unless otherwise noted.
- 3. Lead spacing is measured where the leads emerge from the package.
- 4. Specifications are subject to change without notice.
- 5. The design and working Current for Led is not less than 2mA.

# Absolute Maximum Ratings at TA=25°C

Parameter	Symbol	Value	Unit	
Power Dissipation	PD	120	mW	
Forward Current	IF	30	mA	
Peak Forward Current*1	IFP	100	mA	
Reverse Voltage	VR	5	V	
Operating Temperature	Topr	-40°C To +85°C		
Storage Temperature	Tstg	-40°C To +85°C		
Soldering Temperature*2	Tsol	260°C For 5 S	econds	

Notes:

\*1: Pulse width≤0.1ms, Duty cycle≤1/10

\*2: $\Delta At$  the position of 3mm below package base.

\*3: ▲ Plese refer to the curve of forward current vs.temperature

# Electrical / Optical Characteristics at TA=25°C

Parameter	Symbol	Min.	Тур.	Max	Unit	Test Conditions
Forward Voltage	VF	2.6	3.0	3.4	V	IF=20mA
Reverse Current	IR			10	μA	VR=5V
Dominant Wavelength	λd	514	517	522	nm	IF=20mA
Peak Wavelength	λP		515		nm	IF=20mA
Spectral line Half-width	Δλ		30		nm	IF=20mA
Luminous Intensity	IV	400	840	1700	mcd	IF=20mA
Power Angle	2 <del>0</del> 1/2		29		Deg.	IF=20mA

Remarks:

If special sorting is required (e.g. binning based on forward voltage, luminous intensity, or dominant wavelength), the typical accuracy of the sorting process is as follows:

1.Dominant Wavelength:+/-1nm

2.Chromatic Coordinates:+/-0.01

3. Luminous Intensity: +/-15%

4. Forward Voltage: +/-0.1V

### • VF Rank

Rank	VF	Condition	
Kalik	Min	Мах	Condition
E2F1	2.6	2.8	
F2G1	2.8	3.0	IF=20mA
G2H1	3.0	3.2	IF-2011A
H2I1	3.2	3.4	

Tolerance:±0.1V

### λD Rank

Rank	λD(	Condition	
Kdlik	Min	Мах	Condition
GA	514	516	
GB	516	518	
GC	518	520	IF=20mA
GD	520	522	

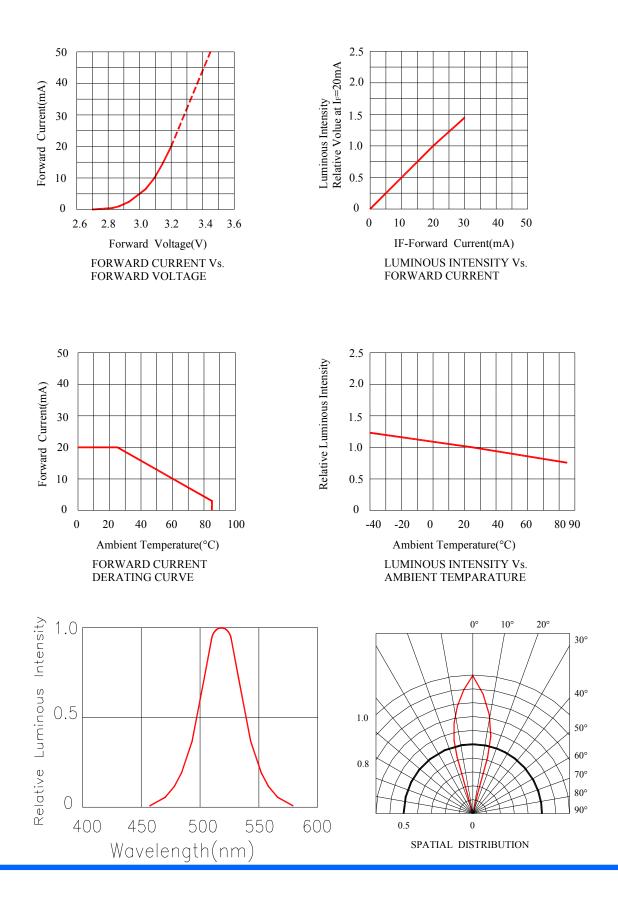
Tolerance:±1nm



Denk	IV(n	Condition	
Rank	Min	Мах	Condition
L	400	600	
М	600	1000	IF=20mA
N	1000	1700	

Tolerance:±15%

# Typical Electrical/Optical Characteristics Curves



# Reliability Test Items and Conditions

Test Classification	Test ltim	Test Conditions	Test Dura- tion	Sample Size	AC/RE
Life Test	Room Temperature DC Operating Life Test	Ta=25°C±5°C, If=20mA	1000hrs	22pcs	0/1
	Thermal Shock Test	100°C±5°C 5min ↓↑ -40°C±5°C 5min	20 cycles	22pcs	0/1
	Temperature Cyle Test	100°C±5°C 30min ↓↑5min -40°C±5°C 30min	20 cycles	22pcs	0/1
Environment Test	High Temperature & High Humidity Test	85°C±5°C /85% RH	1000hrs	22pcs	0/1
	High Temperature Stor- age	Ta=100°C±5°C	1000hrs	22pcs	0/1
	Low temperature Storage	Ta=-40°C±5°C	1000hrs	22pcs	0/1
Mechanical Test	Resistance to Soldering Heat	Temp=260°C ±5°C T=5s max	2 times	22pcs	0/1

# ♦ Criteria for Judging the Damage

Item	Symbol	condition	Criteria for Judgement		
Item	Symbol	condition	MIN.	MAX.	
Forward Voltage	VF (V)	IF=20mA		U.S.L*1.1	
Reverse Current	IR (uA)	VR=5V		10uA	
Luminous Inten- sity	IV (mcd)	IF=20mA	L.S.L*0.5		

[Note] 1.USL:	<b>Upper Specification Level</b>
---------------	----------------------------------

2.LSL: Lower Specification Level

# ◆ CAUTIONS:

## 1.Lead Forming & Assembly

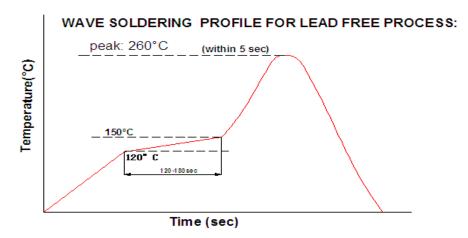
- Lead forming or bending must be done before soldering, at normal temperature.
- During lead forming, the leads should be bent at a point at least 3mm from the base of LED lens.
- Do not use the base of the lead frame as a fulcrum during lead forming.
- Avoid bending the leads at the same point more than once.
- During assembly on PCB, use minimum clinch force possible to avoid excessive mechanicalstress.

## **2.LED Mounting Method**

- •The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement.Lead-forming may be required to insure the lead pitch matches the hols pitch.
- When soldering wire to the LED. Use individual heat-shrink tubing to insulate the exposed leads to prevent accidental coontact short-circuit.
- •Use stand-offs or spacers to securely position the LED above the PCB.

# 3.Soldering

• When soldering, the soldering iron needs to be at least 3mm away from the epoxy edge. After soldering, allow at least 3 minutes for LEDs to cool back to normal temperature.DO not apply any pressure to the epoxy encapsulation or the lead frame during the soldering process.



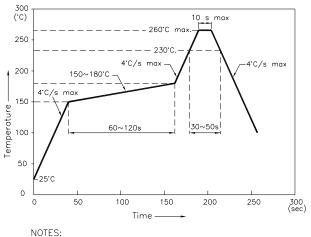
- When using soldering iron .please solder once for less than 5 seconds at a maximum Temperature of 300°C.When soldering a row of LED on a PCB. Please do not solder both Leads of a LED in sequence. (Solder all the positive lead first .then all the negative leads).
- Do not dip the epoxy encapsulation part of LED into any soldering paste liquid.
- After soldering .do not adjust the location of the LED anymore .

• When attaching electronic parts to a PCB with LEDs .the curing time for the whole PCB

Should be less than 60 seconds .at less than a temperature of 120°C.

#### **Soldering Profile**

Reflow Soldering Profile For Lead-free SMT Process.



NOTES: 1.We recommend the reflow temperature 245°C(+/-5°C).The maximum soldering temperature should be limited to 260°C. 2.Don't cause stress to the epoxy resin while it is exposed to high temperature.

3.Number of reflow process shall be 2 times or less.

### 過錫爐條件:IR Reflow 2 次

### 4.Cleaning:

• Use alcohol-based cleaning solvents such as isopropyl alcohol to clean the LEDs if necessary.

## 5.Storage

- The storage ambient for the LEDs should not exceed 30  $^\circ\!C$  temperature or 70% relative humidity.
- It is recommended that LEDs out of their original packaging are used within three months. For extended storage out of their original packaging, it is recommended that the LEDs be stored in a sealed container with appropriate desiccant or in desiccators with nitrogen ambient.

# 6.ESD (Electrostatic Discharge)

Static Electricity or power surge will damage the LED.

Suggestions to prevent of ESD damage.

- All devices, equipment, and machinery must be properly grounded.
- Use a conductive wrist band or anti-electrostatic glove when handling these LEDs.
- Maintain a humidity level of 50% or higher in production areas.
- Use anti-static packaging for transportation and storage.

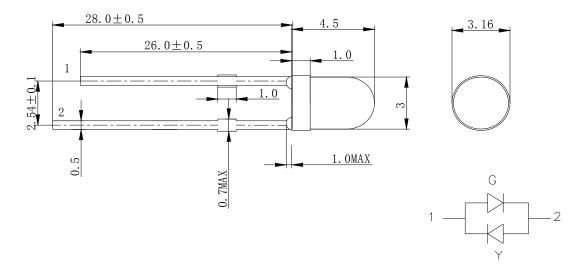
## 7. Recommended Usage Guidelines

- Please only use 20mA(Lamp LED) of forward current to drive LEDs whether one LED or multiple LEDs are being used.
- Sudden surge could damage the LED interior connections.please design circuit with care to no sudden voltage surge or current surge will show when turning the circuit on or off.

#### ♦Features

- •3mm Rounded LED Lamps
- •Emitting Color: Yellow /Green
- •Lens Color: White diffuse
- Mertial:AlGaInP/InGaN
- •Low power consumption
- •Excellent product quality and reliability
- •Lead-free device
- Applications
- Electronic signs and signals
- Bright ambient lighting conditions
- Backlight
- General purpose indicatiors

### • Package Dimensions



Notes:

- 1. All dimensions are in millimeters.
- 2. Tolerance is  $\pm 0.25$  unless otherwise noted.
- 3. Lead spacing is measured where the leads emerge from the package.
- 4. Specifications are subject to change without notice.
- 5. The design and working Current for Led is not less than 2mA.

# • Absolute Maximum Ratings at TA=25°C

Deremeter	Symbol	Va	lue	Unit		
Parameter	Symbol	Yellow	Green	Unit		
Power Dissipation	Pd	80	120	mW		
Forward Current	lf	30	30	mA		
Peak Forward Current*1	<b>I</b> FP	100	100	mA		
Reverse Voltage	VR	5	5	V		
Operating Temperature	Topr	-40°C To +85°C				
Storage Temperature	Tstg	-40°C To +85°C				
Soldering Temperature*2	Tsol		260°C For 5	Seconds		

Notes:

\*1: Pulse width≤0.1ms, Duty cycle≤1/10

\*2: $\Delta$ At the position of 3mm below package base.

\*3: ▲ Plese refer to the curve of forward current vs.temperature

# Electrical / Optical Characteristics at TA=25°C

Parameter	Symbol	Min.	Тур.	Мах	Unit	Test Condi- tions		
Forward Voltage	Yellow	VF	1.8	2.0	2.6	V	IF=20mA	
Torward Voltage	Green	VF	2.7	3.0	3.5	V	II -2011A	
Reverse Current	Yellow	lr			10	μA	VR=5V	
Reverse Current	Green	lr			10	μA	VR-5V	
Dominant Wavelength	Yellow	λd	585	590	595	nm	IF=20mA	
Dominant wavelength	Green	λd	515	518	525	nm	IF-20IIIA	
Peak Wavelength	Yellow	λp		595	_	nm	IF=20mA	
reak wavelengui	Green	λp	-	515	—	nm	IF-2011A	
Spectral line Half-width	Yellow	Δλ	_	16	—	nm	IF=20mA	
	Green	Δλ	_	30	_	nm	1F-2011A	
	Yellow	lv	120	180	400	mcd	IF=20mA	
Luminous Intensity	Green	v	500	1000	2300	mcd		
Power Angle		2 <i>0</i> 1/2	_	G:71 Y:85	_	Deg	IF=20mA	

#### Remarks:

If special sorting is required (e.g. binning based on forward voltage, luminous intensity, or dominant wavelength), the typical accuracy of the sorting process is as follows:

1.Dominant Wavelength:+/-1nm

2.Chromatic Coordinates:+/-0.01

3. Luminous Intensity: +/-15%

4. Forward Voltage: +/-0.1V

VF Rank	Yel	low			
Denk	VF	(V)	Condition		
Rank	Min	Мах	- Condition		
A2B1	1.8	2.0			
B2C1	2.0	2.2	IF=20mA		
C2D1	2.2	2.4	11 -2011A		
D2E1	2.4	2.6			

Tolerance:±0.1V

• λD Rank

Yellow

	λD(	Condition		
Rank	Min	Мах	Condition	
Y2	585	590	IF=20mA	
Y3	590	595	IF-2011A	

Tolerance:±1nm

Yellow IV Rank IV(mcd) Condition Rank Min Max I 120 180 J IF=20mA 180 270 Κ 270 400

Tolerance:±15%

VF Rank	Gree	n	
Rank	VF	(V)	Condition
Ralik	Min	Мах	Condition
F1F2	2.7	2.9	
G1G2	2.9	3.1	IF=20mA
H1H2	3.1	3.3	11 -2011A
1112	3.3	3.5	

Tolerance:±0.1V

λD Rank

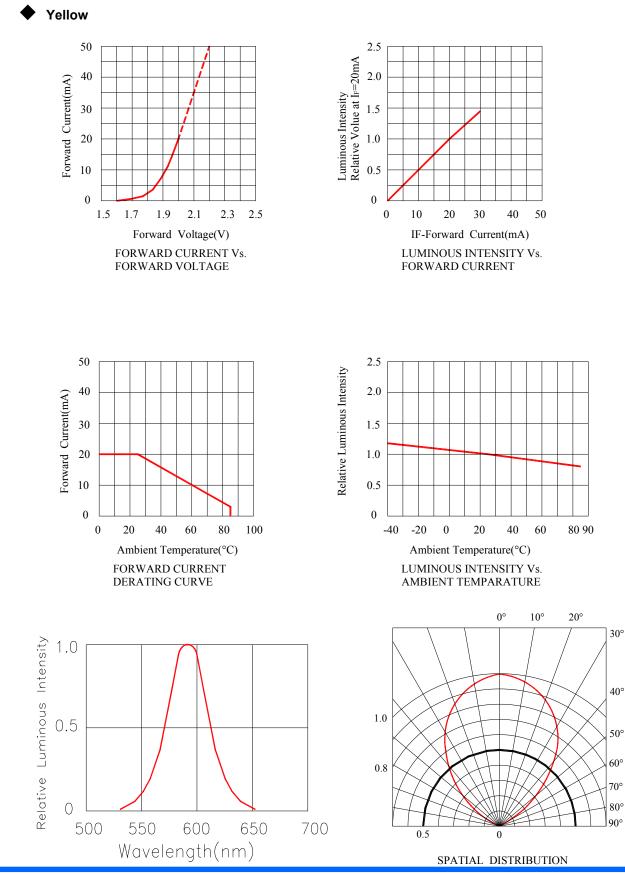
Green

	λD(	0	
Rank	Min	Мах	Condition
G4	515	520	IF=20mA
G5	520	525	IF-2011A

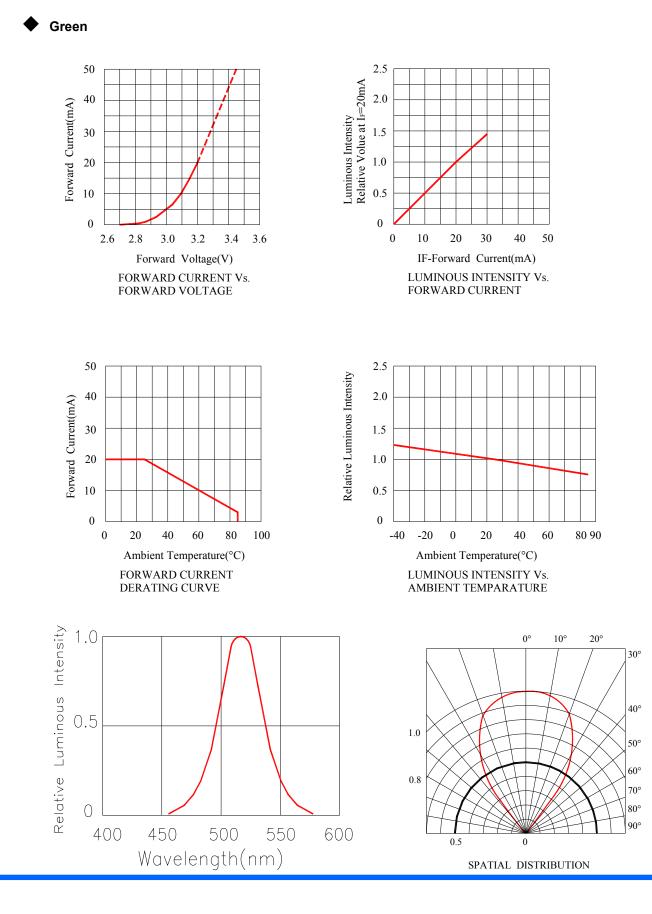
Tolerance:±1nm

Green V Rank IV(mcd) Condition Rank Min Max 1050 Μ 500 IF=20mA Ν 1050 1500 0 1500 2300

Tolerance:±15%



# Typical Electrical/Optical Characteristics Curves



Typical Electrical/Optical Characteristics Curves

# Reliability Test Items and Conditions

Test Classification	Test ltim	Test Conditions	Test Dura- tion	Sample Size	AC/RE
Life Test	Room Temperature DC Operating Life Test	Ta=25°C±5°C, If=20mA	1000hrs	22pcs	0/1
	Thermal Shock Test	100°C±5°C 5min ↓↑ -40°C±5°C 5min	20 cycles	22pcs	0/1
	Temperature Cyle Test	100°C±5°C 30min ↓↑5min -40°C±5°C 30min	20 cycles	22pcs	0/1
Environment Test	High Temperature & High Humidity Test	85°C±5°C /85% RH	1000hrs	22pcs	0/1
	High Temperature Stor- age	Ta=100°C±5°C	1000hrs	22pcs	0/1
	Low temperature Storage	Ta=-40°C±5°C	1000hrs	22pcs	0/1
Mechanical Test	Resistance to Soldering Heat	Temp=260°C ±5°C T=5s max	2 times	22pcs	0/1

# ♦ Criteria for Judging the Damage

Item	Symbol	condition	Criteria for Judgement			
Item	Symbol	condition	MIN.	MAX.		
Forward Voltage	VF(V)	IF=20mA		U.S.L*1.1		
Reverse Current	IR (uA)	VR=5V		10uA		
Luminous Inten- sity	IV (mcd)	IF=20mA	L.S.L*0.5			

2.LSL: Lower Specification Level

# ◆ CAUTIONS:

## 1.Lead Forming & Assembly

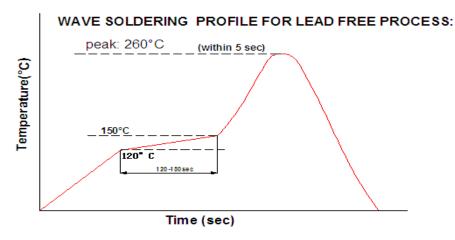
- Lead forming or bending must be done before soldering, at normal temperature.
- During lead forming, the leads should be bent at a point at least 3mm from the base of LED lens.
- Do not use the base of the lead frame as a fulcrum during lead forming.
- Avoid bending the leads at the same point more than once.
- During assembly on PCB, use minimum clinch force possible to avoid excessive mechanicalstress.

# 2.LED Mounting Method

- •The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement.Lead-forming may be required to insure the lead pitch matches the hols pitch.
- When soldering wire to the LED. Use individual heat-shrink tubing to insulate the exposed leads to prevent accidental coontact short-circuit.
- •Use stand-offs or spacers to securely position the LED above the PCB.

# 3.Soldering

• When soldering, the soldering iron needs to be at least 3mm away from the epoxy edge. After soldering, allow at least 3 minutes for LEDs to cool back to normal temperature.DO not apply any pressure to the epoxy encapsulation or the lead frame during the soldering process.



• When using soldering iron .please solder once for less than 5 seconds at a maximum Temperature of 300°C.When soldering a row of LED on a PCB. Please do not solder both

Leads of a LED in sequence. (Solder all the positive lead first .then all the negative leads).

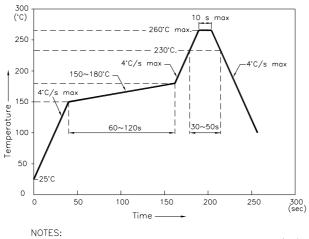
- Do not dip the epoxy encapsulation part of LED into any soldering paste liquid.
- After soldering .do not adjust the location of the LED anymore .

• When attaching electronic parts to a PCB with LEDs .the curing time for the whole PCB

Should be less than 60 seconds .at less than a temperature of 120°C.

#### **Soldering Profile**

Reflow Soldering Profile For Lead-free SMT Process.



NOTES: 1.We recommend the reflow temperature 245°C(+/-5°C).The maximum soldering temperature should be limited to 260°C. 2.Don't cause stress to the epoxy resin while it is exposed to high temperature.

3.Number of reflow process shall be 2 times or less.

### 過錫爐條件:IR Reflow 2 次

### 4.Cleaning:

• Use alcohol-based cleaning solvents such as isopropyl alcohol to clean the LEDs if necessary.

### 5.Storage

- The storage ambient for the LEDs should not exceed 30  $^\circ C$  temperature or 70% relative humidity.
- It is recommended that LEDs out of their original packaging are used within three months. For extended storage out of their original packaging, it is recommended that the LEDs be stored in a sealed container with appropriate desiccant or in desiccators with nitrogen ambient.

## 6.ESD (Electrostatic Discharge)

Static Electricity or power surge will damage the LED. Suggestions to prevent of ESD damage.

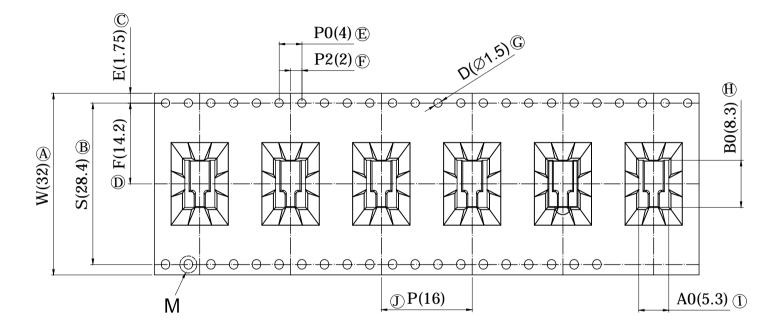
- All devices, equipment, and machinery must be properly grounded.
- Use a conductive wrist band or anti-electrostatic glove when handling these LEDs.
- Maintain a humidity level of 50% or higher in production areas.
- Use anti-static packaging for transportation and storage.

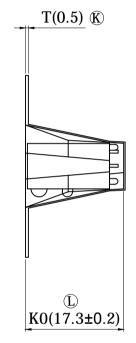
### 7. Recommended Usage Guidelines

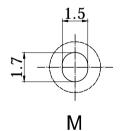
- Please only use 20mA(Lamp LED) of forward current to drive LEDs whether one LED or multiple LEDs are being used.
- Sudden surge could damage the LED interior connections.please design circuit with care to no sudden voltage surge or current surge will show when turning the circuit on or off.

ITEM	W	A0	B0	D	Е	F	S	K0	P0	P2	Р	Т
DIM	32	5.3	8.3	1.5	1.75	14.2	28.4	17.3	4	2	16	0.5
TOLE	±0.3	±0.1	±0.1	+0.1 -0	±0.1	<b>±0</b> .1	±0.1	±0.2	<b>±0</b> .1	±0.1	<b>±</b> 0.1	±0.05

DIMENSION	TOLERANCE				
BELOW 10 mm	± 0.3				
10~100 mm	± 0.5				
ABOVE 100 mm	± 0.8				
ANGLE ± 3°					
CRITICAL DIMENSIONS : 'A' ~ 'L'					



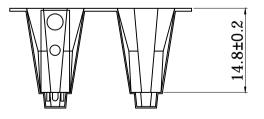




NOTE:

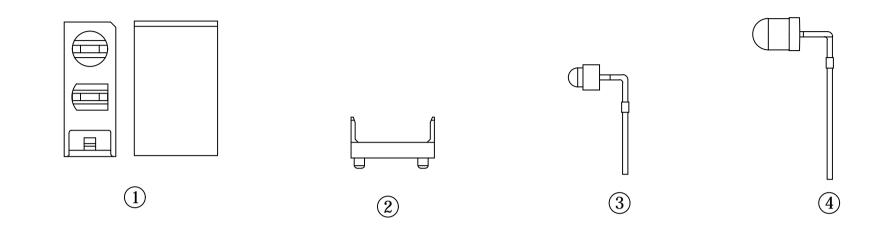
1.10 SPROCKET HOLE PITCH CUMULATIVE TOLERANCE±0.2
2.CARRIER CAMBER IS 1MM IN 100MM
3.A0 AND B0 MEASURED ON A PLANCE 0.3MM ABOVE THE BOTTOM OF THE POCKET
4.K0 MEASURED FROM A PLANE ON THE INSIDE BOTTOM OF THE POCKET TO THE TOP SURFACE OF THE CARRIER
5.ALL DIMENSIONS MEET EIA-481-3 REQUIREMENTS
6.PACKING LENGTH PER 13"REEL : 3.8 METERS

7.COMPONENT LOAD PER 13"REEL : 200 PCS



A					DATA	2020/03/12	UNIT	mm	MODE		
2					APPROVAL	KAVEN	SCALE	1:1	PART	LED003 料带图	<b>IX/D</b> Well Buying
$\triangle$					CONFIRM	EASON	VIEW	$\oplus$	2D FILE NAME	LED003 料带图	<b>VVD</b> Industrial Co.,Ltd.
	DATA	APPROVAL	DESIGN	ENGINEERING CHANGE DESCRIPTION	DESIGN	YJS	VER.	01	3D FILE NAME		

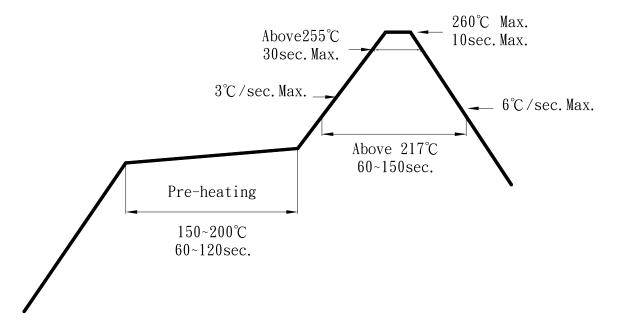
DIMENSION	TOLERANCE
BELOW 10 mm	± 0.3
10-100 mm	± 0.5
ABOVE 100 mm	± 0.8
ANGLE	± 3°



 $\Box$ 

				لالطلاح										
							NO.	PART N	RT NAME		MATERIAL		SPECIAL DEAL	RoHS REPORT No.
							1	FRAME		1 PA6T BLACK		BLACK	CE-2019-10882	
							2	BASE FRAME		1	PA6T		BLACK	CE-2019-10882
							3	LED2		1			GREEN	
							4	LED1		1			YELLOW/GREE	N
$\triangle$					DATA	2019/12/26	UNIT		mm	MODE	Ξ			
⚠					APPROVAL	KAVEN	SCAL	.E	1:1	PART	Г LED00		3-KYGUG-TR	<b>M</b> / <b>W</b> ell Buying
$\triangle$					CONFIRM	EASON	VIEW	I	\$€				3-KYGUG-TR ERIALS LIST	WB <sup>Well Buying</sup> Industrial Co.,Ltd.
	DATA	APPROVAL	DESIGN	ENGINEERING CHANGE DESCRIPTION	DESIGN	YJS	VER.		01	3D FII	LE NAME			

### ●IR-Reflow Soldering



- 1. Avoid any external stress applied to the resin while the LEDs are at high temperature, especially during soldering.
- 2. Avoid rapid cooling or any excess vibration during temperature ramp-down process
- 3. Although the soldering condition is recommended above,

soldering at the lowest possible temperature is feasible for the LEDs

- 4. Conform with JEDEC J-STD-020 with MSL 3.
- 5. Maximum times of IR Reflow process: 2 times.



350℃ Within 3 sec.,One time only.